

IN THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Canceled)

2. (Currently Amended) A display device ~~with comprising~~ a plurality of pixels arranged in a pixel portion,

wherein ~~pixels of the plurality of pixels are arranged in each pixel column~~ and two or more data lines ~~extend~~ are placed in each of the plural pixels[.] ~~to simultaneously supply video signals to the pixels through the data lines.~~

3. (Original) A display device according to claim 2,

wherein the pixels each have a switching element and a light emitting element, and wherein the switching element is connected to one of the two or more data lines, which is predetermined for each pixel.

4. (Currently Amended) A display device comprising:

~~a plurality of pixels arranged in a matrix pattern in a pixel portion, the matrix pattern having a plurality of pixel columns in which the pixels are arranged in a column direction;~~
~~a plurality of data lines extending in the column direction;~~
~~a plurality of scanning lines extending in a row direction ; and ,~~

~~a plurality of pixels arranged into a matrix pattern, the pixels each having a light emitting element,~~

wherein the pixels each have a light emitting element;

~~wherein x data lines (x is a natural number equal to or larger than 2 [[4]]) out of the plural the plurality of data lines extend are placed in each column each of the pixel columns and one scanning line out of the plural scanning lines extends is placed in each row,~~

~~wherein y scanning drivers (y is a natural number equal to or larger than 1) are provided to select x scanning lines out of the plural scanning lines simultaneously, and~~

~~wherein x data drivers are provided to simultaneously supply signals to x pixels selected out of the plurality of the plural pixels through the x data lines extending placed in each column each of the pixel columns.~~

5. (Currently Amended) A display device comprising:

a plurality of pixels arranged in a matrix pattern in a pixel portion, the matrix pattern having a plurality of pixel columns in which the pixels are arranged in a column direction;

~~x data lines (x is a natural number equal to or larger than 2 [[4]]) placed in each pixel column;~~

~~one scanning line placed in each row; and~~

~~a plurality of pixels placed at points portions where the data lines and the scanning line intersect with each other to form a matrix pattern, the pixels each having a light emitting element,~~

~~wherein y scanning drivers (y is a natural number equal to or larger than 1) are provided to select x scanning lines out of the plural the plurality of scanning lines simultaneously, and~~

wherein x data drivers are provided to simultaneously supply signals to x pixels selected out of the plurality of the plural pixels through the x data lines extending placed in each pixel column.

6. (Currently Amended) A display device according to claim 4, wherein the x data drivers each have a plurality of shift registers and sampling circuits, and the shift registers each operating independently, each of the sampling circuits being associated with one of the shift registers.

7. (Currently Amended) A display device according to claim 5, wherein the x data drivers each have a plurality of shift registers and sampling circuits, and the shift registers each operating independently, each of the sampling circuits being associated with one of the shift registers.

8. (Original) A display device according to claim 4, wherein the x data drivers each have a plurality of shift registers, first latches, second latches, and sampling circuits, the shift registers each operating independently, each of the first latches, each of the second latches, and each of the sampling circuits being associated with one of the shift registers.

9. (Original) A display device according to claim 5, wherein the x data drivers each have a plurality of shift registers, first latches, second latches, and sampling circuits, the shift registers each operating independently, each of the first latches, each of the second latches, and each of the sampling circuits being associated with one of the shift registers.

10. (Original) A display device according to claim 3, wherein the light emitting element comprises an OLED.

11. (Original) A display device according to claim 4, wherein the light emitting element comprises an OLED.

12. (Original) A display device according to claim 5, wherein the light emitting element comprises an OLED.

13. (Currently Amended) A display device according to claim 4, wherein the plural pixels, the y scanning drivers, and the x data drivers are formed over [[on]] the same insulator.

14. (Currently Amended) A display device according to claim 5, wherein the plural pixels, the y scanning drivers, and the x data drivers are formed over [[on]] the same insulator.

15. (Original) A display device according to claim 4, wherein the pixels each have a driving transistor, a switching transistor, and a capacitor, the driving transistor controlling a current value of the light emitting element, the switching transistor controlling input of a video signal into its pixel, and the capacitor holding the video signal.

16. (Original) A display device according to claim 5, wherein the pixels each have a driving transistor, a switching transistor, and a capacitor, the driving transistor controlling a

current value of the light emitting element, the switching transistor controlling input of a video signal into its pixel, and the capacitor holding the video signal.

17. (Original) A display device according to claim 4, wherein the pixels each have a driving transistor, a switching transistor, a capacitor, and an erasing transistor, the driving transistor controlling a current value of the light emitting element, the switching transistor controlling input of a video signal into its pixel, the capacitor holding the video signal, and the erasing transistor discharging electric charges that are held in the capacitor.

18. (Original) A display device according to claim 5, wherein the pixels each have a driving transistor, a switching transistor, a capacitor, and an erasing transistor, the driving transistor controlling a current value of the light emitting element, the switching transistor controlling input of a video signal into its pixel, the capacitor holding the video signal, and the erasing transistor discharging electric charges that are held in the capacitor.

19. (Currently Amended) A driving method of a display device that has a plurality of pixels arranged in a matrix pattern in a pixel portion, the matrix pattern having a plurality of pixel columns in which the pixels are arranged in a column direction; a plurality of data lines in [[a]] the column direction, a plurality of scanning lines in a row direction, ~~and a plurality of pixels arranged into a matrix pattern, wherein~~ the pixels each ~~having~~ have a light emitting element, x data lines (x is a natural number equal to or larger than 2) out of ~~the plural the plurality of~~ data lines extended ~~being placed in each column each of the pixel columns~~, and one

scanning line out of the plurality the plural scanning lines extending being placed in each row, comprising the steps of:

wherein dividing one frame period has into a plurality of sub-frame periods,

wherein dividing each of the plural sub-frame periods each have into a writing period and a light emission period, or a writing period, a light emission period, and an erasure period, and

wherein, in the writing period, selecting x scanning lines simultaneously by y scanning drivers (y is a natural number equal to or larger than 1) select x scanning lines simultaneously whereas x data drivers simultaneously supply simultaneously supplying signals by the x data drivers to x pixels selected out of the plural the plurality of pixels through the x data lines extending placed in each column each of the pixel columns.

20. (Currently Amended) A driving method of a display device that has a plurality of pixels arranged in a matrix pattern in a pixel portion, the matrix pattern having a plurality of pixel columns in which the pixels are arranged in a column direction; x data lines placed in each of the pixel columns, one scanning line placed in each row column, and the [[a]] plurality of pixels placed at points portions where the data lines and the scanning line intersect to each other to form the [[a]] matrix pattern, wherein the pixels each have having a light emitting element[[,]] :comprising the steps of:

wherein dividing one frame period has into a plurality of sub-frame periods,

wherein dividing each of the plural sub-frame periods each have into a writing period and a light emission period, or a writing period, a light emission period, and an erasure period, and

wherein, in the writing period, selecting x scanning lines simultaneously by y scanning drivers (y is a natural number equal to or larger than 1) select x scanning lines simultaneously

whereas ~~x data drivers simultaneously supply simultaneously supplying signals by the x data drivers~~ to x pixels selected out of the plural the plurality of pixels through the x data lines extending placed in each column each of the pixel columns.

21. (New) A display device comprising:

a plurality of data lines;

a plurality of scanning lines;

a plurality of pixels arranged in rows and columns;

at least first and second pixel electrodes arranged in one of the columns;

a first data line operationally connected to the first pixel electrode;

a second data line operationally connected to the second pixel electrode;

a first scanning line of the plurality of scanning lines intersecting with the first data line;

a second scanning line of the plurality of scanning lines intersecting with the second data

line; and

wherein the pixels each a light-emitting element.

22 (New) A display device according to claim 21, wherein the light emitting element comprises an OLED.

23. (New) A display device comprising:

a plurality of pixels;

a plurality of data lines;

a plurality of scanning lines;

a first data driver for supplying a video signal to the pixels which are arranged in first to $m/2$ -th rows and in odd-numbered rows;

a second data driver for supplying a video signal to the pixels which are arranged in first to $m/2$ -th rows and in even-numbered rows;

a third data driver for supplying a video signal to the pixels which are arranged in $(m/2+1)$ -th to m -th rows and in odd-numbered rows;

a fourth data driver for supplying a video signal to the pixels which are arranged in $(m/2+1)$ -th to m -th rows and in even-numbered rows;

a first scanning driver for controlling the scanning lines extending in the first to $m/2$ -th rows;

a second scanning driver for controlling the scanning lines extending in the $(m/2+1)$ -th to m -th rows; and

at least two data lines of the plurality of data lines extend in each column pixel.

24. (New) A display device comprising:

a plurality of pixels;

a plurality of data lines;

a plurality of scanning lines;

a first data driver for supplying a video signal to the pixels arranged in a m -th row;

a second driver for supplying a video signal to the pixels arranged in a $(m+1)$ -th row;

a third data driver for supplying a video signal to the pixels arranged in a $(m+2)$ -th row;

a fourth data driver for supplying a video signal to the pixels arranged in a $(m+3)$ -th row;

a first driver for controlling the scanning line extending in the m -th row;

a second driver for controlling the scanning line extending in the $(m+1)$ -th row;
a third driver for controlling the scanning line extending in the $(m+2)$ -th row; and
a fourth driver for controlling the scanning line extending in the $(m+3)$ -th row.

25. (New) A display device comprising:

a plurality of pixels;
a plurality of data lines;
a plurality of scanning lines;
a first data driver for supplying a video signal to the pixels which are arranged in first to $m/4$ -th rows through the data lines;
a second data driver for supplying a vide signal to the pixels which are arranged in $(m/4+1)$ -th rows to $m/2$ -th row through the data lines;
a third data driver for supplying a vide signal to the pixels which are arranged in $(m/2+1)$ -th row to $3 \times m/4$ -th rows through the data lines;
a fourth data driver for supplying a vide signal to the pixels which are arranged in $(3 \times m/4+1)$ -th row to m -th rows through the data lines;
a first scanning driver for controlling the scanning lines extending in the first to $m/4$ -th rows;
a second scanning driver for controlling the scanning lines extending in the $(m/4+1)$ -th row to $m/2$ -th rows;
a third scanning driver for controlling the scanning lines extending in the $(m/2+1)$ -th row to $3 \times m/4$ -th row;

a fourth scanning driver for controlling the scanning lines extending in the $(3 \times m/4 + 1)$ -th row to m -th row; and

at least two data lines of the plurality of data lines extend in each column pixel.

26. (New) A display device according to claim 24, wherein each of the first to fourth data drivers includes a plurality of shift registers which operate independently, a plurality of first latches, a plurality of second latches, and a plurality of sampling circuits.

27. (New) A display device according to claim 25, wherein each of the first to fourth data drivers includes a plurality of shift registers which operate independently, a plurality of first latches, a plurality of second latches, and a plurality of sampling circuits.

28. (New) A display device according to claim 26, wherein each of the first to fourth data drivers includes a plurality of shift registers which operate independently, a plurality of first latches, a plurality of second latches, and a plurality of sampling circuits.

29. (New) A display device according to claim 24, wherein the first scanning driver and the second scanning driver each includes a shift register and a buffer.

30. (New) A display device according to claim 25, wherein the first to fourth scanning drivers each includes a shift register and a buffer.

31. (New) A display device according to claim 26, wherein the first to fourth scanning drivers each includes a shift register and a buffer.

32. (New) A display device according to claim 24, wherein the plurality of pixels, the plurality of data lines, the plurality of scanning lines, the plurality of first data drivers, the plurality of second data drivers, the plurality of third data drivers, the plurality of fourth data drivers, the plurality of first scanning drivers and the plurality of second scanning drivers are formed over a same insulator.

33. (New) A display device according to claim 25, wherein the plurality of pixels, the plurality of data lines, the plurality of scanning lines, the plurality of first data drivers, the plurality of second data drivers, the plurality of third data drivers, the plurality of fourth data drivers, the plurality of first scanning drivers and the plurality of second scanning drivers, the plurality of third scanning drivers, and the plurality of fourth scanning drivers are formed over a same insulator.

34. (New) A display device according to claim 26, wherein the plurality of pixels, the plurality of data lines, the plurality of scanning lines, the plurality of first data drivers, the plurality of second data drivers, the plurality of third data drivers, the plurality of fourth data drivers, the plurality of first scanning drivers and the plurality of second scanning drivers, the plurality of third scanning drivers, and the plurality of fourth scanning drivers are formed over a same insulator.